

In place of PTO-1449 Form	U. S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	Complete if Known
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number <u>N/A 10/814096</u>
		Filing Date <u>Herewith 3/31/04</u>
		Applicant(s) <u>Lysander Lim</u>
		Art Unit <u>N/A 2816</u>
		Examiner Name <u>N/A Dinh Le</u>
SHEET <u>1</u> OF <u>3</u>	Attorney Docket Number <u>23119.8</u>	

U. S. PATENT DOCUMENTS				
Examiner's Initials	Cite No.	Document Number	Publication Date <small>MM-DD-YYYY</small>	Name of Patentee or Applicant of Cited Document
<div style="font-size: 2em; margin-bottom: 10px;">DZ</div> <div style="font-size: 4em; margin-top: 10px;">/</div>	1	6,137,372	10-24-2000	Welland
	2	6,147,567	11-14-2000	Welland et al.
	3	6,150,891	11-21-2000	Welland et al.
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	7	6,304,146	10-16-2001	Welland
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	24	6,580,376	06-17-2003	Perrott
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	27	6,630,868	10-07-2003	Perrott et al.

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Examiner's Initials	Cite No.	Foreign Patent Document <small>(Country Code - Number - Kind)</small>	Publication Date <small>MM-DD-YYYY</small>	Patentee or Applicant of Cited Document	Translation <small>Y/N</small>

OTHER PRIOR ART		
Examiner's Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item, date, page(s), volume-issue number(s), publisher, city/country where published
<div style="font-size: 2em; margin-bottom: 10px;">DZ</div> <div style="font-size: 4em; margin-top: 10px;">/</div>	28	ABRAMOVITCH, Danny, "Phase-Locked Loops: A Control Centric Tutorial", (May 8, 2002), pp.1-50, Agilent Laboratories, Communications and Optics Research Lab.
	29	ALFKE, Peter, "Frequency/Phase Comparator for Phase-Locked Loops", Application Note, (December 2, 1996), pp.1-2, Version 1.1, Xilinx, Inc.

Examiner Signature <u><i>[Signature]</i></u>	Date Considered <u>9/6/26/05</u>
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	N/A 10/814026
				Filing Date	Herewith 3/31/04
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				Art Unit	N/A 2816
				Examiner Name	N/A Dinh G
SHEET	2	OF	3	Attorney Docket Number	23119.8

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	31	ANADIGM, INC., "Understanding Switched Capacitor Basics", Anadigmvortex Technical Training, (November 2002), pp. 1-25.
	32	CRANINCKX, et al., "A Fully Integrated CMOS DCS-1800 Frequency Synthesizer", (1998), pp. 1-10, Institute of Electrical and Electronics Engineers, Inc.
	33	CURTIN, et al., "Phase-Locked Loops for High-Frequency Receivers and Transmitters - Part 1", (1999), pp. 1-4, 33-3, Analog Devices, Inc.
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	37	DE MUER, Bram, "Monolithic CMOS Fractional- N Synthesizer", (November 2002), pp. 142-144, Katholieke Universiteit Leuven.
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	42	LACANETTE, Kerry, "A Basic Introduction to Filters - Active, Passive, and Switched-Capacitor", National Semiconductor Application Note, (April 1991), pp. 1-22, National Semiconductor Corporation.
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<i>DL</i>	48	PHILIPS SEMICONDUCTOR, "An Overview of the Phase-Locked Loop (PLL)", Application Note AN177, (December 1988), pp.1-6.
<i>DL</i>	49	SINHA, Saurabh "Design of an Integrated CMOS PLL Frequency Synthesizer", (2002) Signal Processing Systems, Department of Electrical Engineering, Technische Universiteit Eindhoven.
<i>DL</i>	50	UNIVERSITY OF SEOUL, "Discrete-Time Filter (Switched-Capacitor Filter)", (1996- 2000), IC Lab, Institute of Electrical and Electronics Engineers, Inc.

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